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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/600,245

06/20/2003

Sandeep Bhatia

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EXAMINER

VO, TUNG T

ART UNIT

PAPER NUMBER

2621

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DELIVERY MODE

06/04/2010

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b> 10/600,245	<b>Applicant(s)</b> BHATIA, SANDEEP	
	<b>Examiner</b> Tung Vo	<b>Art Unit</b> 2621	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 03 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 02/18/2010.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-11 and 16-20 is/are pending in the application.
- 4a) Of the above claim(s) 12-15 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-11 and 16-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 20 June 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)          | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____                                      |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)          | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____  | 6) <input type="checkbox"/> Other: _____                          |

**DETAILED ACTION**

***Reopening of Prosecution After Appeal Brief or Reply Brief***

1. In view of the appeal brief filed on 02/18/2010, PROSECUTION IS HEREBY REOPENED. The rejection is set forth below.

To avoid abandonment of the application, appellant must exercise one of the following two options:

(1) file a reply under 37 CFR 1.111 (if this Office action is non-final) or a reply under 37CFR 1.113 (if this Office action is final); or,

(2) initiate a new appeal by filing a notice of appeal under 37 CFR 41.31 followed by an appeal brief under 37 CFR 41.37. The previously paid notice of appeal fee and appeal brief fee can be applied to the new appeal. If, however, the appeal fees set forth in 37CFR 41.20 have been increased since they were previously paid, then appellant must pay the difference between the increased fees and the amount previously paid.

A Supervisory Patent Examiner (SPE) has approved of reopening prosecution by signing below:

/Mehrdad Dastouri/

Supervisory Patent Examiner, Art Unit 2621

***Claim Rejections - 35 USC § 102***

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2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

3. Claims 1-11 and 16-21 are rejected under 35 U.S.C. 102(e) as being anticipated by Abelard et al. (US 7,130,526).

Re claims 1 and 5, Abelard discloses a system for displaying images on a display (fig. 1), said system comprising:

a decoder (9 of fig. 1, a video decoder, col. 4, lines 14-20) for decoding encoded images and parameters associated with the images (DECODING MANAGER of fig. 1, there are

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parameters for decoding as shown in figure 5, I, B, P parameters), thereby resulting in decoded images and decoded parameters associated with the decoded images (26 of fig. 1, A, B, and C are decoded memories to store the decoded image and the decoded parameters, fig. 7, A memory stored I2, P8, B"1, B"0, P5, B9, P5, I'2, P'2, P'8, B1, B0 accordance to decoding per frame period);

image buffers (26 of fig. 1, A, B, C are decoded image buffers) for storing the decoded images;

a FIFO (DISPLAY MANAGER of fig. 2, col. 5, lines 37-42; col. 5, lines 43-col. 6, line 10; fig. 3) for storing indicators indicating images to be displayed (col. 6, lines 39-51, identifying the next picture to be displayed); and

a display engine (14 of fig. 1) for presenting the images indicated by the FIFO for display.

Re claims 2 and 6, Abelard further discloses the A, B, and C buffers (26 of fig. 1) buffer inherently has parameter buffers for storing the decoded parameters (I, B, P parameters as shown in figure 5) associated with the images (e.g. I, B, and P picture).

Re claims 3 and 7, Abelard further discloses wherein the display engine (14 of fig. 1) presents the images indicated by the queue for display by receiving the decoded parameters and displaying the decoded images based on the decoded parameters.

Re claim 4, Abelard further discloses wherein the decoder comprises a first processor (9 of fig. 1) and the display engine comprises a second processor (14 of fig. 1).

Re claim 8, Abelard discloses a circuit (fig. 1) for displaying images on a display (14 of fig. 1), said circuit comprising:

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a processor (10 of fig. 1);

a memory (24 of fig. 1) operably coupled to the processor,

said memory storing a plurality of executable instructions (col. 4, lines 21-24),

wherein the plurality of executable instructions (figs. 2-6) cause:

decoding encoded images and parameters associated with the images (figs. 3 and 5),

thereby resulting in decoded images and decoder parameters associated with the decoded images (fig. 4);

storing the decoded images (26 of fig. 1);

storing indicators indicating images to be displayed in a FIFO (DISPLAY MANAGER of fig. 2, note the Display Manager checks every 40 ms which picture is to be displayed. In other words, it identifies the reconstruction buffer containing the picture to be displayed, Col. 5, line 54-col.6, line 20 ); and

presenting the images indicated by the stored indicators for display (14 of fig. 1, see fig. 3).

Re claim 9, Abelard further discloses storing the decoded parameters (I, B, P parameters as shown in figure 5) associated with the images (A, B, and C of fig. 7).

Re claim 10, Abelard further discloses wherein the instructions causing presenting the images further comprise instructions causing receiving the decoded parameters and displaying the decoded images based on the decoded parameters (fig. 3).

Re claim 11, Abelard further discloses a circuit (fig. 1) for displaying images on a display, said circuit comprising:

a first processor (10 of fig. 1);

a first memory (24 of fig. 1) operably coupled to the first processor,  
said first memory storing a plurality of instructions for execution by the first processor  
(col. 4, lines 21-24),

wherein the plurality of executable instructions cause ( Note Receiver 1 also comprises a reprogrammable non-volatile memory 24, which holds the receiver's operating system, device drivers and other software modules. The receiver's software is executed by the microprocessor):  
decoding encoded images and parameters associated with the images (DECODER MANAGER of fig. 2, note Video Decoding Manager has previously received through a queue from the Overall Control a complete command ordering and specifying the decoding and/or the display of this particular picture. Based on this command, the Video Decoding Manager programs the decoding of the newly detected picture), thereby resulting in decoded images and decoder parameters associated with the decoded images (e.g. fig. 7, note A, B, and C buffers are storing the decoded images and decoded parameters that are I2, B'3, P5 associated with the pictures) ;

storing the decoded images (A, B, and C of fig. 1);

storing indicators indicating images to be displayed in a FIFO (DISPLAY MANAGER of fig. 2) ; and

a second processor (14 of fig. 1) operably coupled to the queue (col. 5, line 53-col. 6, line7);

a second memory (DISPLAY MANAGER of fig. 2 for storing a software descriptor, col. 5, lines 43-46) operably coupled to the second processor (14 of fig. 1), said second memory storing a plurality of instructions (fig. 6, there are instructions) for execution by the second processor (e.g. fig. 6), wherein the plurality of executable instructions cause:

presenting the images indicated by the indicators for display (figs. 3 and 6).

Re claims 16 and 21, Abelard further discloses wherein the FIFO (DISPLAY MANAGER of fig. 2) stores the indicators (queue) in the particular order prior to the display engine displaying the images associated with the indicators in the order corresponding to the order that the indicators are stored in the FIFO (see also fig. 7).

Re claims 17-20, Abelard further discloses wherein each indicator indicates a different image to be displayed (fig. 3, I, B, or P picture is displayed; see fig. 7).

### ***Conclusion***

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Willis et al (US 6,154,603) discloses picture decoding for trick mode operation.

### ***Contact Information***

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tung Vo whose telephone number is 571-272-7340. The examiner can normally be reached on Monday-Wednesday, Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mehrdad Dastouri can be reached on 571-272-7418. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.



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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Tung Vo/

Primary Examiner, Art Unit 2621